

FIG. 1

000000 20280000

134

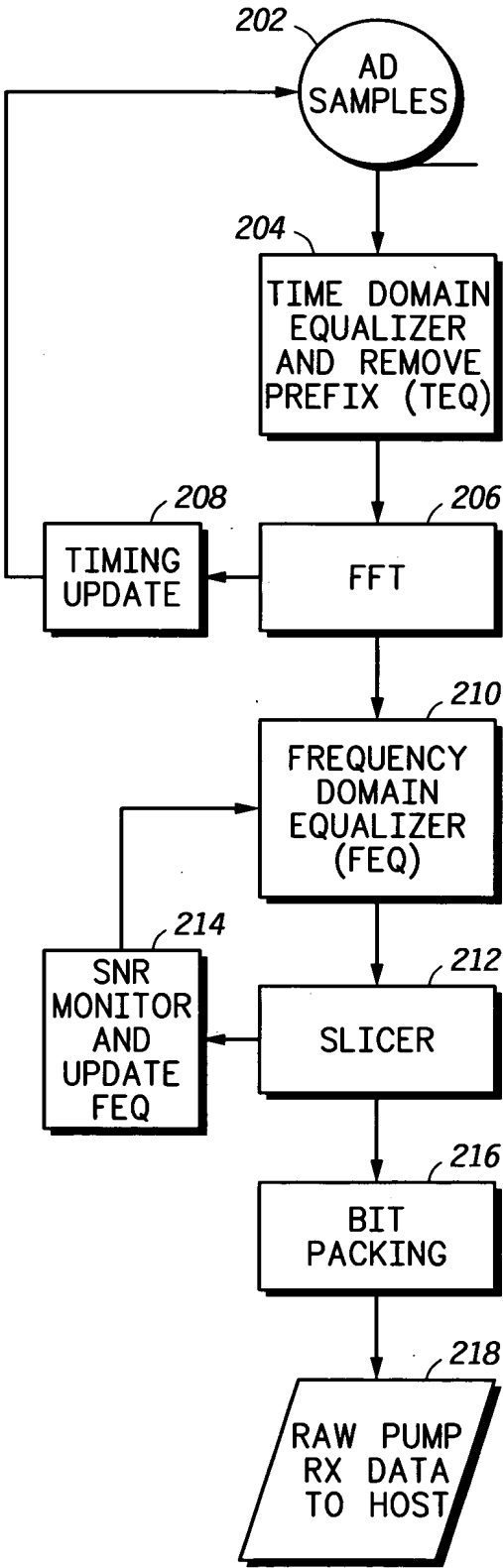


FIG.2

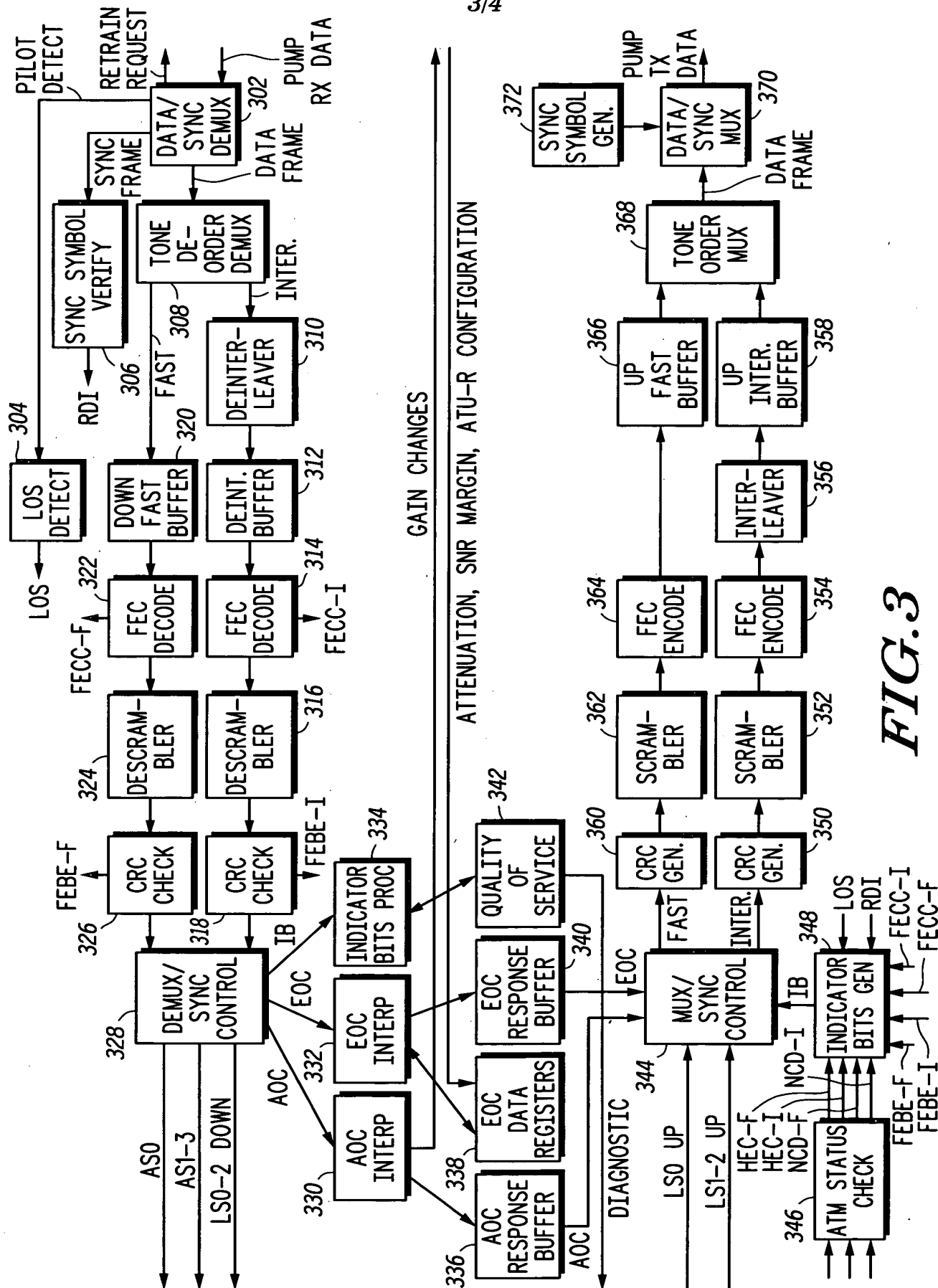


FIG.3

400

	HOST	DSP	
0-T	SENDS SYMBOLS A-C IN A BURST OVER PCI BUS	QUEUES A, B, C IN INCOMING DATA BUFFER	402 C B A
T-2T	X	QUEUES A, B, C IN INCOMING DATA BUFFER	404 X C B
2T-3T	X	QUEUES A, B, C IN INCOMING DATA BUFFER	406 X X C
3T-4T	SEND SYMBOL D	QUEUES A, B, C IN INCOMING DATA BUFFER	408 X X D
4T-5T	X	SEND SYMBOL D	410 X X X
5T-6T	X	RESEND SYMBOL D, INCREMENT COUNTER TO 1	412 X X X
6T-7T	SEND SYMBOLS E, F, G	RESEND SYMBOL D, QUEUE SYMBOLS E, F, G, INCREMENT COUNTER TO 2	414 G F E
7T-8T	SEND SYMBOLS H, I	THROW AWAY E AND F BASED ON COUNTER AND SEND SYMBOL G, QUEUE SYMBOLS H AND G	416 I H

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FIG. 4